

Patent Assignment Abstract of Title

Total Assignments: 1

Application #: <u>10014359</u>	Filing Dt: 12/14/2001	Patent #: NONE
PCT #: NONE		Issue Dt:
		Publication #: <u>US20020080825</u>
		Pub Dt: 06/27/2002

Inventors: Michael Joachim Wolf, Werner Beisel, Jurgen Hohn

Title: Method and compensation module for the phase compensation of clock signals

Assignment: 1

Reel/Frame: <u>012379/0160</u>	Received: 12/21/2001	Recorded: 12/14/2001	Mailed: 02/11/2002
			Pages: 2

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignors: <u>WOLF, MICHAEL JOACHIM</u>	Exec Dt: 11/28/2001
<u>BEISEL, WERNER</u>	Exec Dt: 11/28/2001
<u>HOEHN, JUERGEN</u>	Exec Dt: 11/28/2001

Assignee: ALCATEL
54 RUE LA BOETIE
75008 PARIS, FRANCE

Correspondent: SUGHRUE, MION PLLC
DAVID J. CUSHING
2100 PENNSYLVANIA AVENUE NW
SUITE 800
WASHINGTON, D.C. 20037-3213

Search Results as of: 8/22/2005 3:05:45 P.M.

If you have any comments or questions concerning the data displayed, contact OPR / Assignments at 703-308-9723
Web interface last modified: Oct. 5, 2002